

## PERSONAL INFORMATION

Name **SAHAKYAN ARTHUR**  
Address Apt. 46, Bagratunyats 21, 0046, Yerevan, Armenia  
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E-mail **Personal: [art.sah.stp@gmail.com](mailto:art.sah.stp@gmail.com) , Corporative: [arthurs@synopsys.com](mailto:arthurs@synopsys.com)**  
Nationality Armenian  
Date of birth 31/ Aug/1991



## WORK EXPERIENCE

- Dates (from – to) May 2015 – Till Now
- Name and address of employer Piloyan Mikayel, Vahan Galstyan
- Type of business or sector Synopsys Armenia, CJSC  
41 Arshakunyats Ave., Bldg #1  
0026 Yerevan, Armenia
- Occupation or position held **Sr. A&MS Circuit Design Engineer**  
I did some projects connecting with mix-signal and analog design systems. Works on complex problems/projects where analysis of situation or data requiring an evaluation of intangible variance factors. I can work on problems that impact the design success of current products or technologies. I've an experience in the analog IP design (such as: DAC, ADC, USB, PCIe, etc.), especially in the SERDES area and strong background in the CMOS analog design and semiconductor physics. I've good analysis and problem-solving skills and an experience of EDA tool usage.
- Occupation or position held **R&D Engineer II**  
March 2013 – May 2015
- Occupation or position held **R&D Engineer I**  
February 2011 – March 2013
- Dates (from – to) August 2011 – February 2011
- Name and address of employer Vazgen Melikyan
- Type of business or sector Synopsys Educational Department Working Group
- Occupation or position held **Intern**

## EDUCATION AND TRAINING

- Dates (from – to)
- Name and type of organization providing education and training
- Principal subjects/occupational skills covered
- Title of qualification awarded

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### ***Bachelor's degree***

01/Sep/2008 – 18/May/2012

- State Engineering University of Armenia (SEUA)
- Synopsys Armenia Educational Department (SAED)

Semiconductor Physics, Semiconductor Devices, Semiconductor Technologies, Analog IC Design, Digital IC Design, RF IC Design, Digital Design Processing, Microprocessor Systems

Bachelor's degree in Engineering in the field of Electronic Means (SEUA)

Bachelor's degree in Engineering in the field of VLSI Specialization (SAED)

### ***Master's degree***

01/Sep/2012 – 31/May/2014

- State Engineering University of Armenia (SEUA)
- Synopsys Armenia Educational Department (SAED)

RF IC Design, Special I/O Design, Mixed-signal IC Design, Scripting languages: PERL/TCL

Master's degree in Engineering in the field of VLSI Specialization (SAED)

Master's degree in Engineering in the field of Electronic Means (SEUA)

### ***PhD degree***

12/June/2014 – 15/May/2017

National Polytechnic University of Armenia (NPUA)

Special I/O Design, Mixed-signal IC Design

PhD degree in Engineering in the field of VLSI Specialization (SAED)

PhD degree in Engineering in the field of Micro and Nano electronics.

## SCIENTIFIC ARTICLES

1. A.S. Sahakyan, A.G. Hayrapetyan, H.H. Dingchyan, "Architecture Of Self-Configurable Delay-Locked Loop By Frequency Range", 9th International Conference of Semiconductor Micro and Nano Electronics, Yerevan, Armenia, May 24-26, 2013
2. Vazgen Melikyan, Arthur Sahakyan, Aram Shishmanyanyan, Nazeli Melikyan, "Data - Clock Setup and Hold Times Margins Correction Methods in High Speed Serial Links", CSIT International Conference, Yerevan, Armenia, September 23-27, 2013
3. Vazgen Melikyan, Arthur Sahakyan, Artak Hayrapetyan, Abraham Balabanyan, Milena Stanojlović, Grigor Zargaryan, "Serializer/Deserializer Output Data Signal Duty Cycle Correction Method", 57th ETRAN Conference, Zlatibor, Serbia, June 3-6, 2013, pp. EL3.4.1-4
4. Vazgen Melikyan, Arthur Sahakyan, Safaryan Karo, Dingchyan Hayk, "High Accuracy Equalization Method for Receiver Active Equalizer", EWDTs'2013 Conference, Rostov-on-Don, Russia, September 27 – 30, 2013
5. Vazgen Melikyan, Arthur Sahakyan, Gagik Hovhannisyan, Hayk Dingchyan, Artak Hayrapetyan, Vahe Babayan, Ashot Martirosyan, "Low Voltage Current Mirror with a High Output Voltage Swing"
6. Vazgen Melikyan, Arthur Sahakyan, Armen Sahakyan, Hayk Dingchyan, "High PSRR and Accuracy Receiver Active Equalizer", ELNANO International Conference, Kiev, Ukraine, 2014
7. V. Melikyan, A. Sahakyan, M. Piloyan, A. Shishmanyanyan, A. Hovhannisyan, D. Trdatyan, "Resistance Calibration Method Without External Precision Elements", Proceedings of IEEE East-West Design & Test Symposium (EWDTs'2013), Kharkov National University of Radio-electronics, Rostov-on-Don, Russia

MOTHER TONGUE

ARMENIAN

OTHER LANGUAGES

- Reading skills
- Writing skills
- Verbal skills

RUSSIAN	ENGLISH
Very Good	Very Good
Very Good	Very Good
Very Good	Very Good

ARTISTIC SKILLS  
AND COMPETENCES  
*Music, writing, design, etc.*

I attend the football, swimming and painting clubs.

DRIVING LICENCE(S)

B